SHA-1 ARITHMETIC OPERATION CORE CIRCUIT

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ECC PROCESSING CIRCUIT
REGISTER GROUP
ECC ARITHMETIC
OPERATION CORE
CIRCUIT

REGISTER GROUP

ECC
ARITHMETIC
OPERATION
CORE
CORE
CIRCUIT
CIRCUIT

CIRCUIT

FIG. 1C

FIG. 1B

CIRCUIT

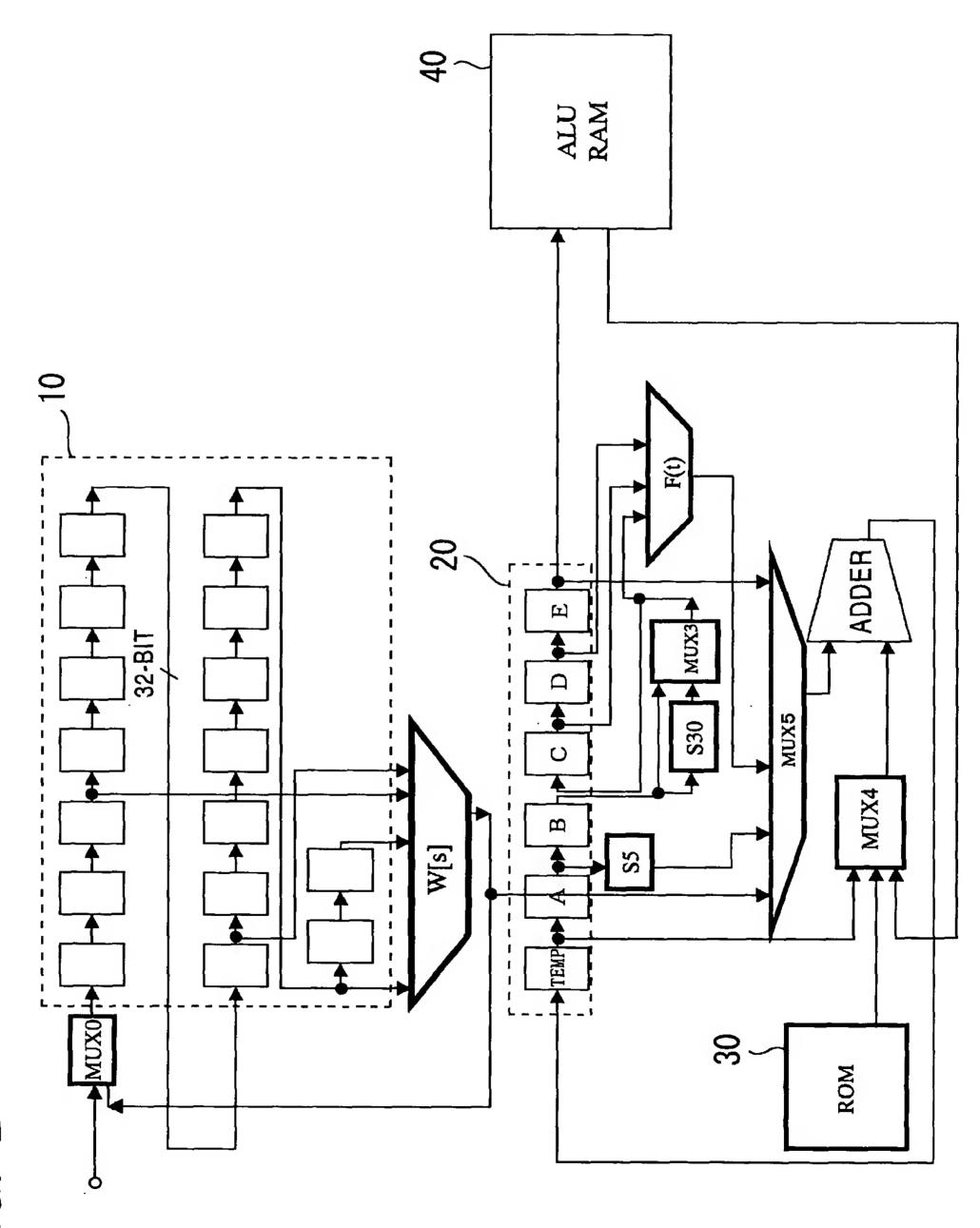
**PROCESSING** 

SHA-1

GROUP

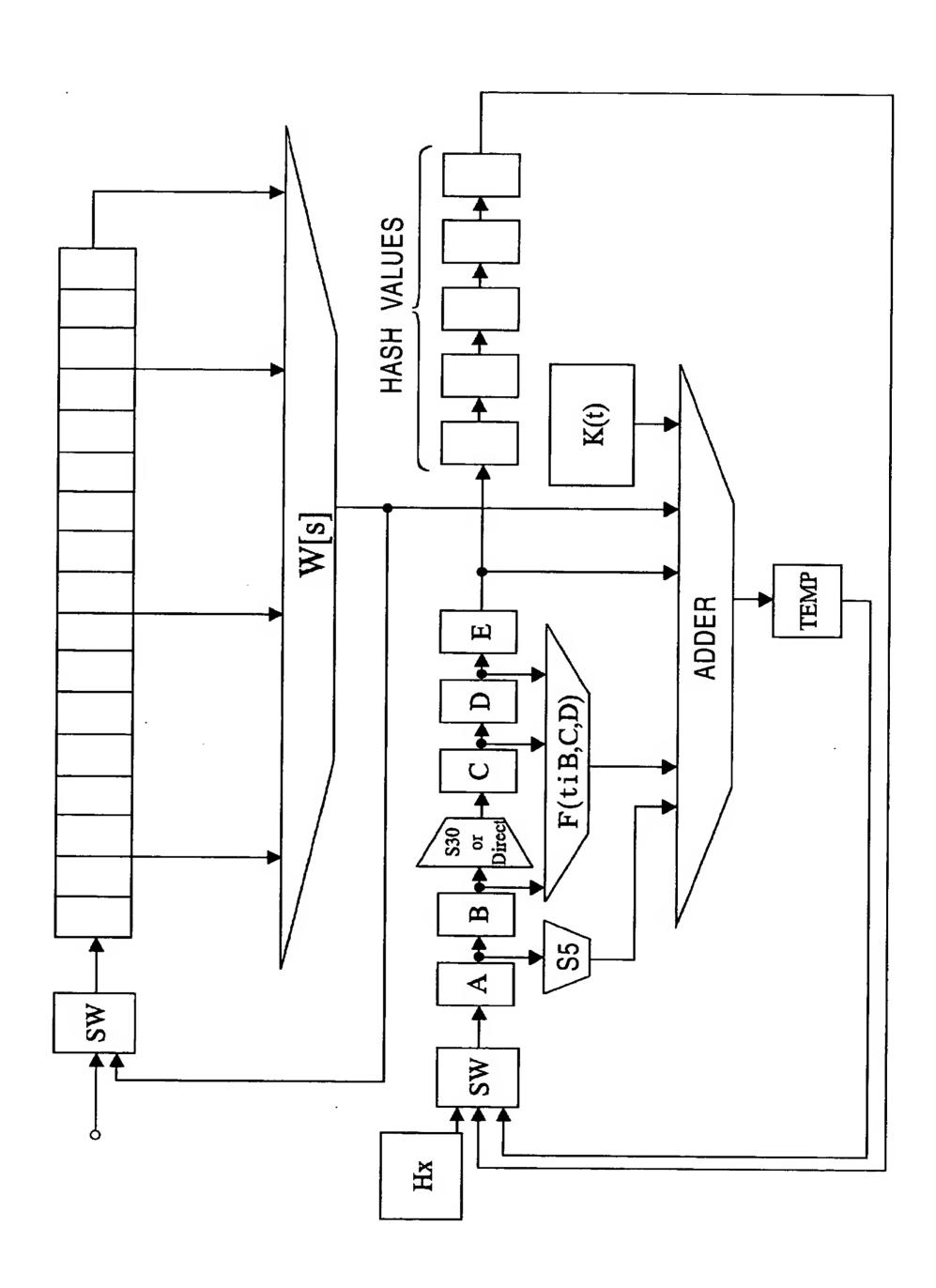
REGISTER

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<u>E</u>

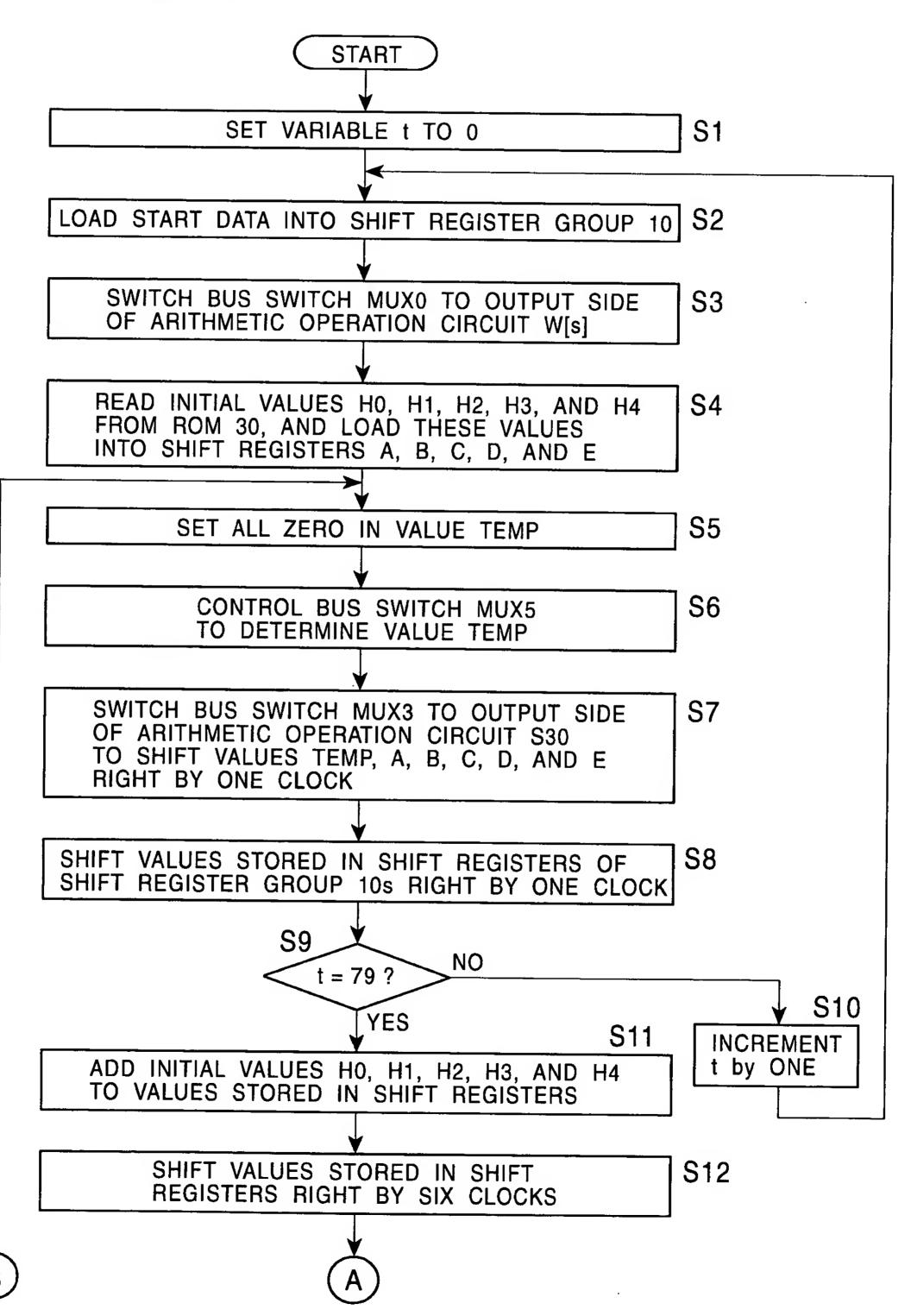
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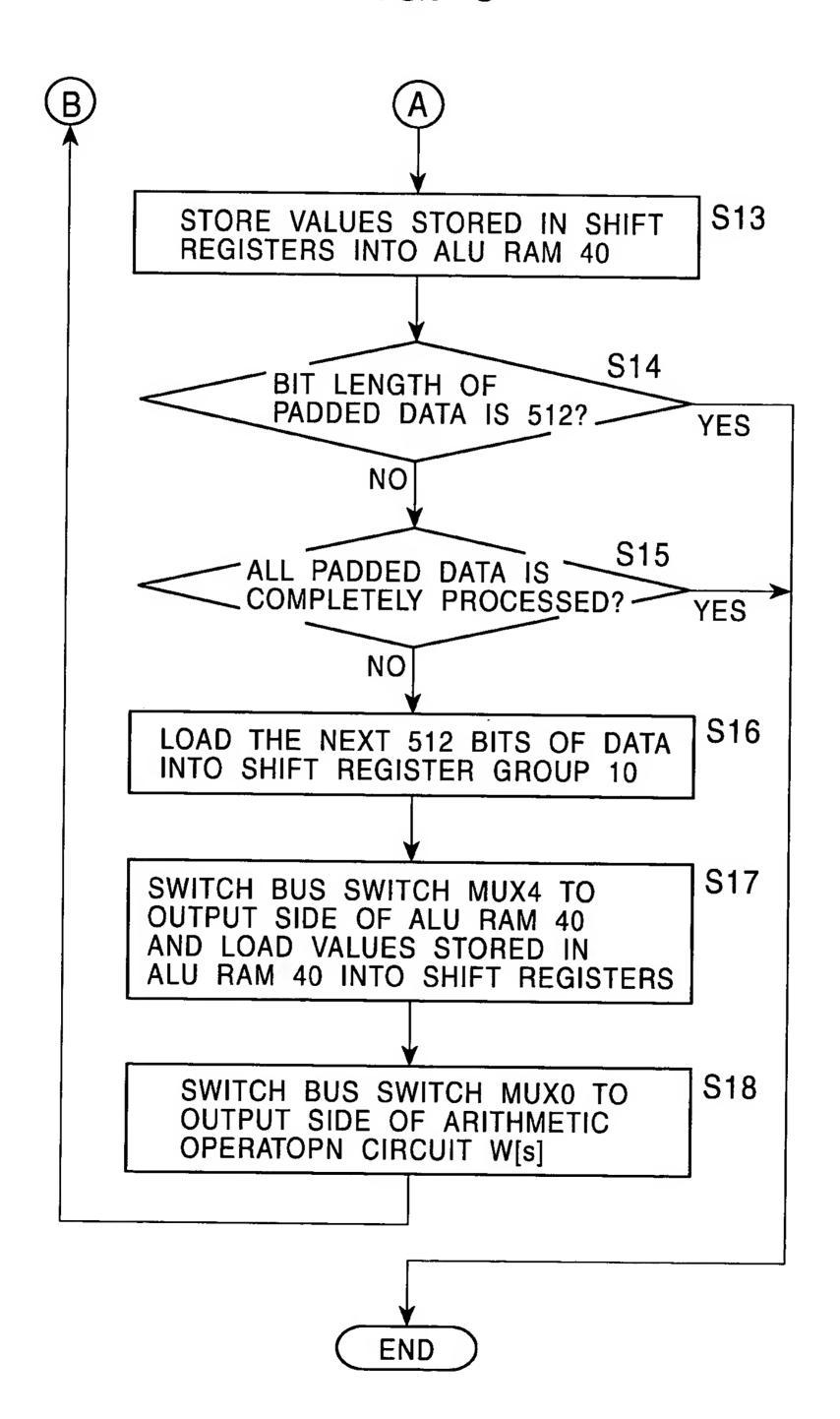
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FIG. 4

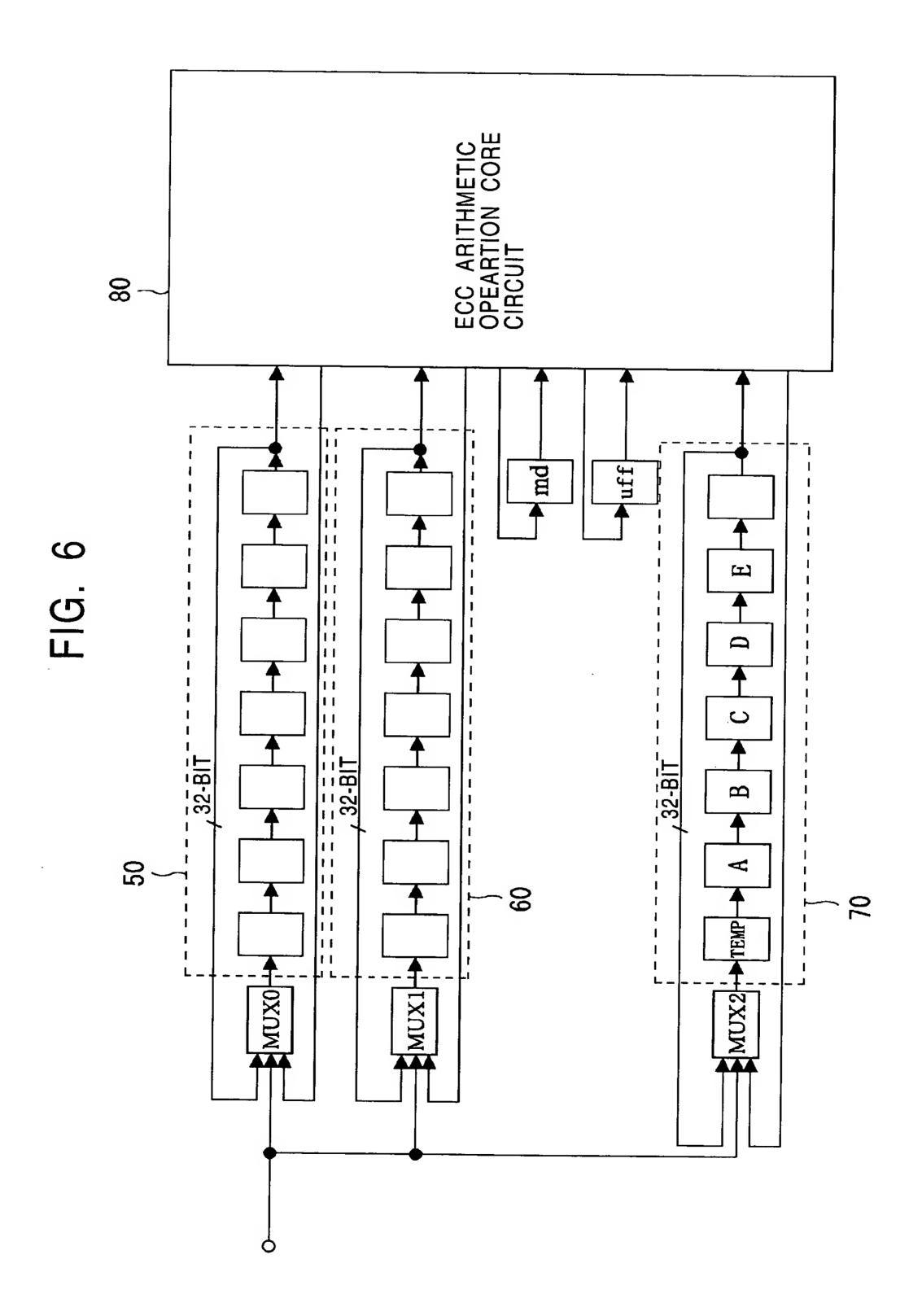


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FIG. 5



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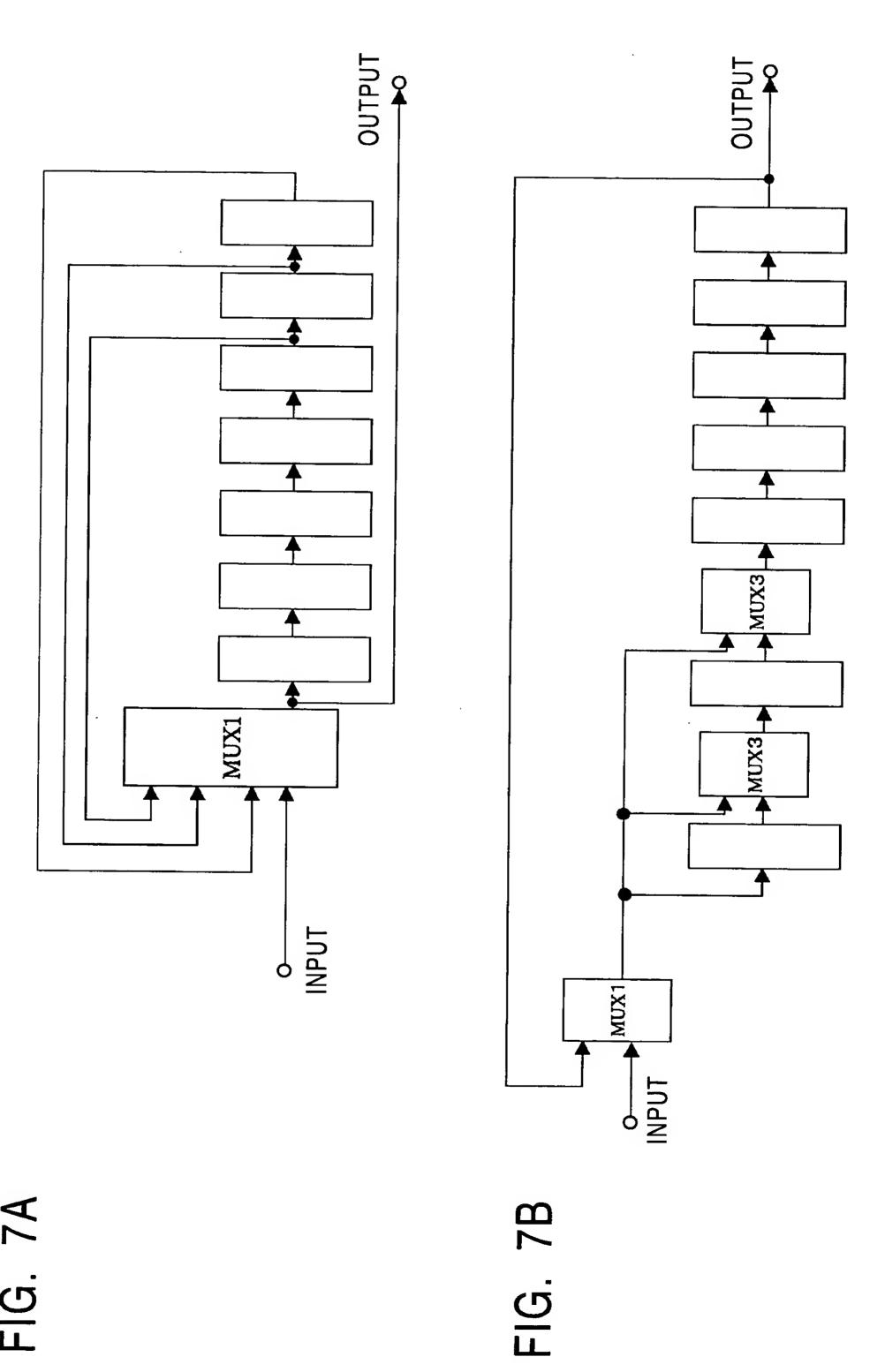
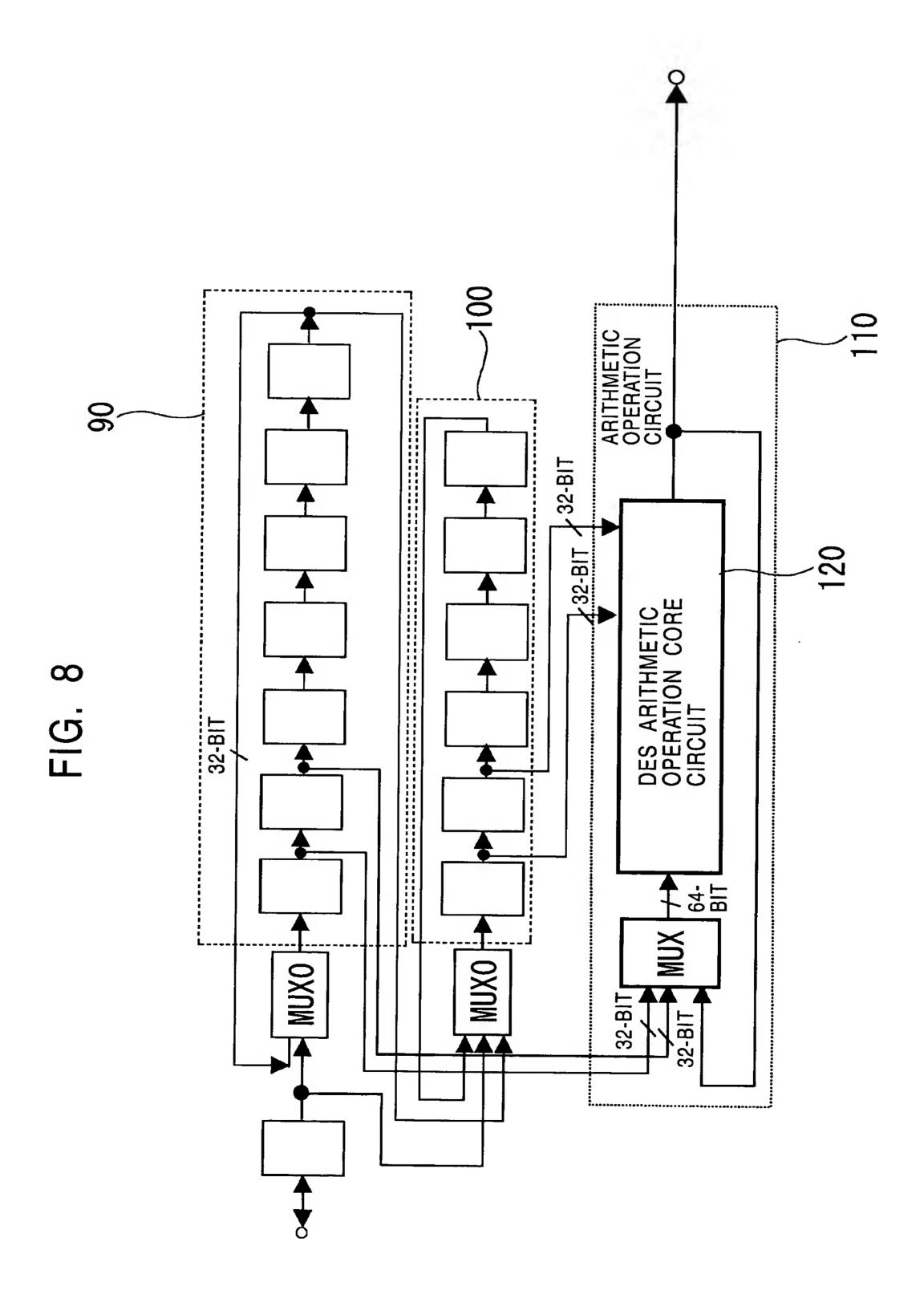
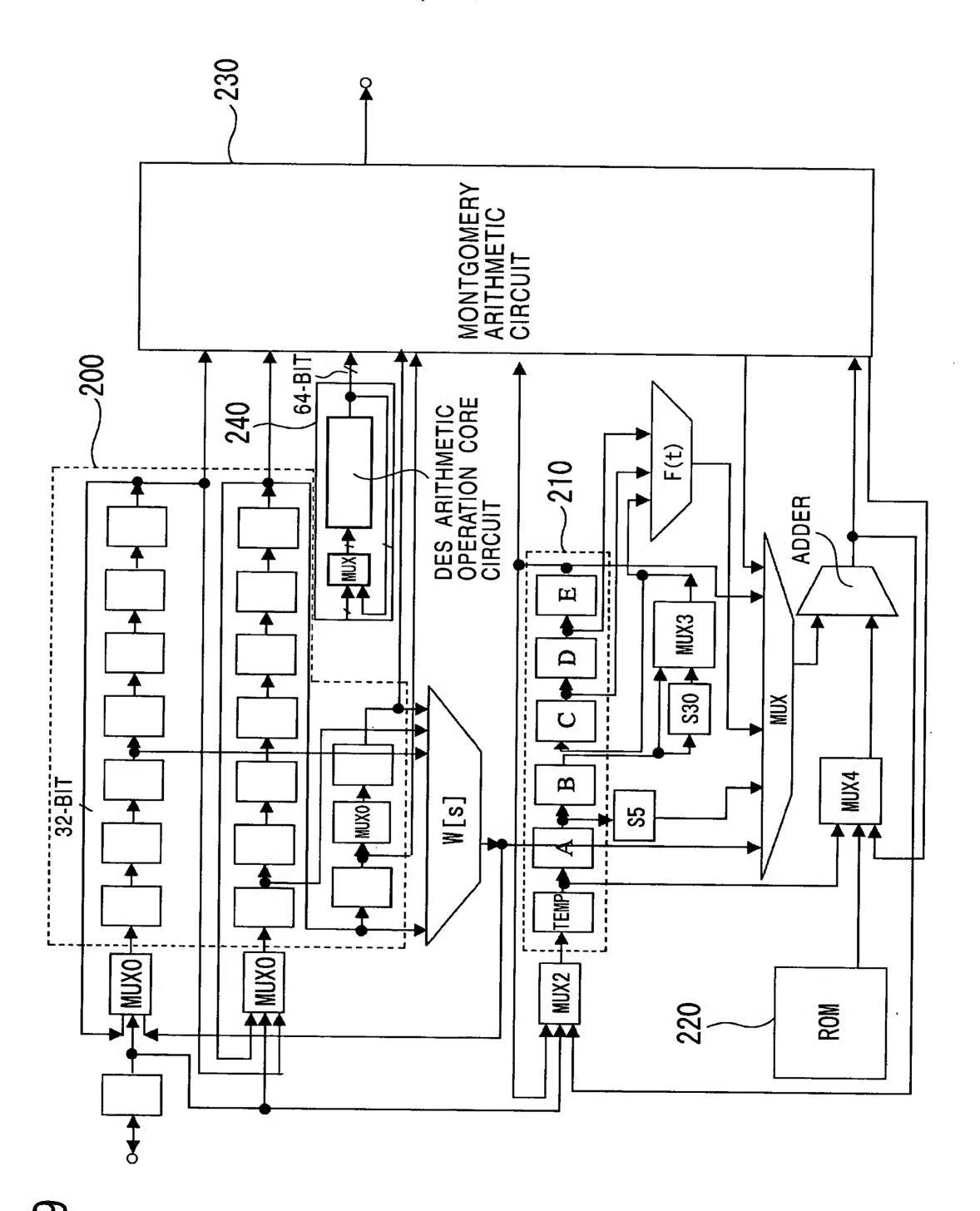


FIG. 7A

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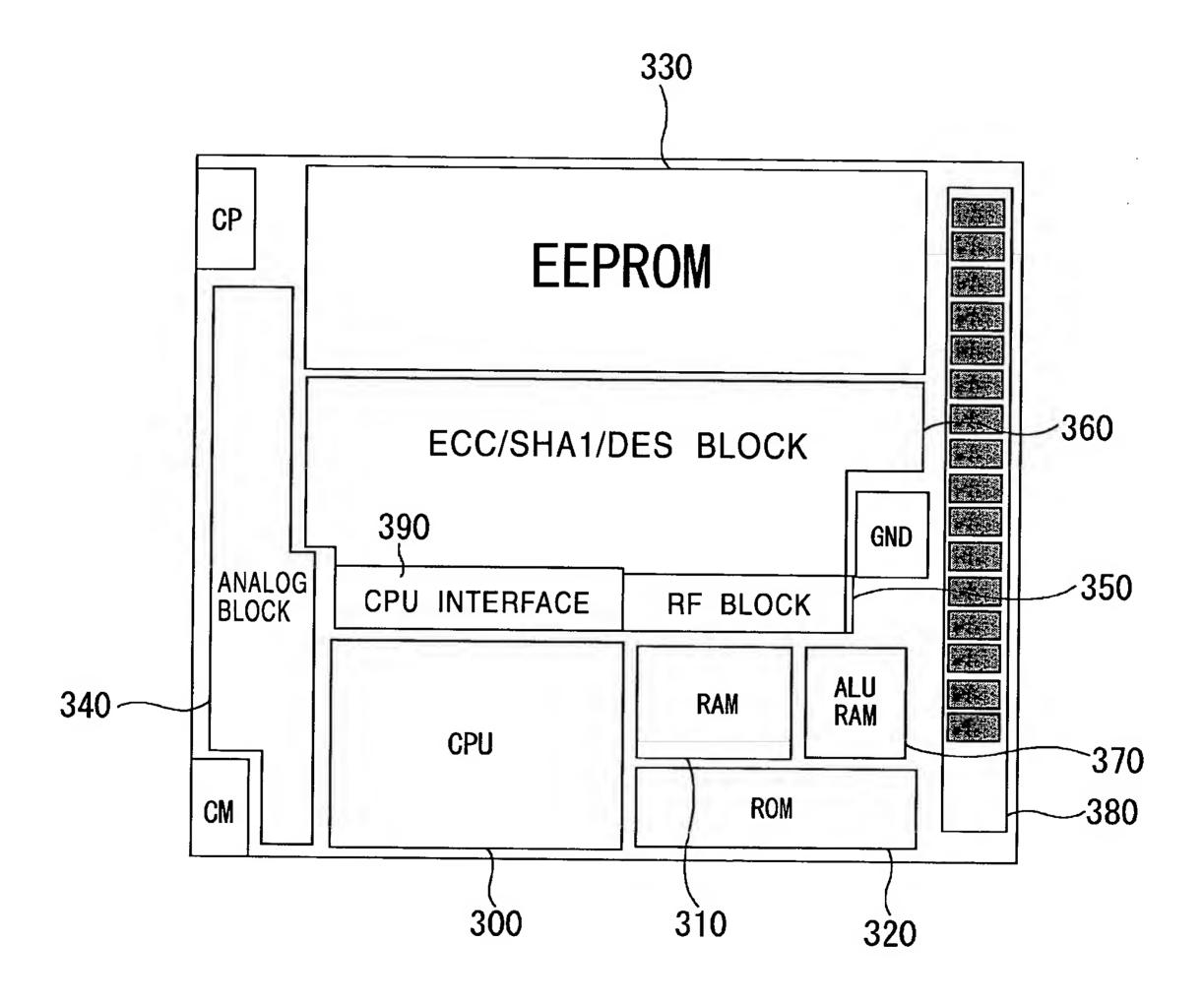
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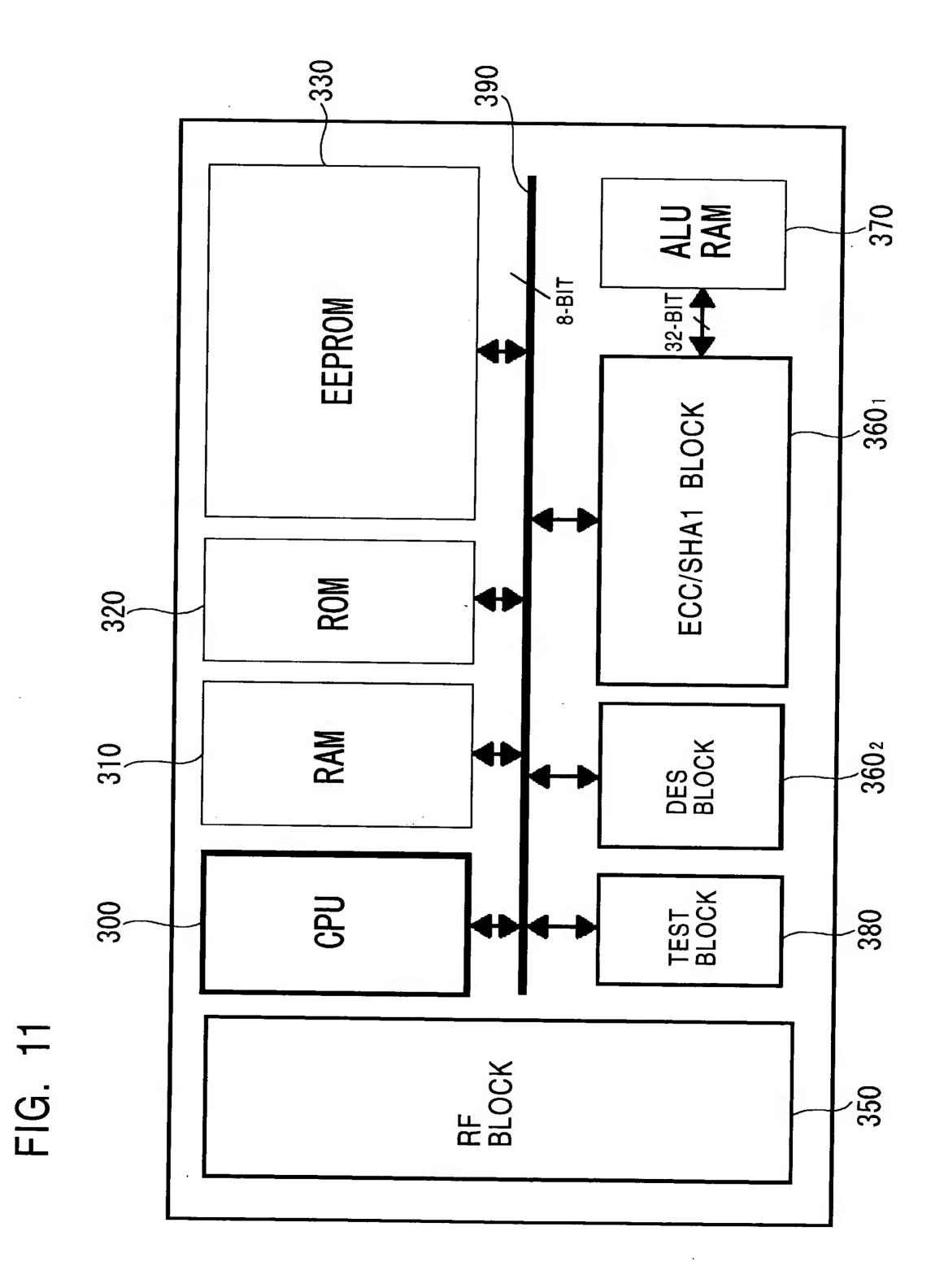
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FIG. 10



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